

video and audio data signals over a rf link comprising:

a first digital signal processor for converting analog video signals into digital video signals and for compressing the digital video signals into video bytes;

5 a second digital signal processor for decompressing received digital video bytes into digital video signals and for converting the decompressed digital video signals into analog video signals;

D1
concl
10 a third digital signal processor for converting analog audio signals into digital audio signals, for compressing the [audio] digital audio signals into audio bytes, for decompressing received audio bytes into [audio] digital audio signals, and for converting the decompressed digital audio signals into analog audio signals;

means for periodically refreshing the transmitted video signals;

means for running multiple compression and decompression algorithms on all three digital signal processors;

15 a solid state memory; and

means for emulating a disk access system of a computer using solid state memory components to store file sequences with compression/decompression algorithm data.

D2
20
concl
22. (Amended) The narrowband codec of claim [19] 21 wherein the power supply is between 19 and 36 volts.

D3
cont
29. (Twice Amended) A narrowband video codec for transmitting and receiving compressed video and audio data signals over a rf link comprising:

a first digital signal processor for converting analog video signals into digital video
5 signals and for compressing the digital video signals into video bytes;

a second digital signal processor for decompressing received digital video bytes into
digital video signals and for converting the decompressed digital video signals into analog video
signals;

a third digital signal processor for converting analog audio signals into digital audio
10 signals, for compressing the [audio] digital audio signals into audio bytes, for decompressing
received audio bytes into [audio] digital audio signals, and for converting the decompressed
digital audio signals into analog audio signals;

means for periodically refreshing the transmitted video signals;

means for running multiple compression and decompression algorithms on all three
15 digital signal processors;

a solid state memory;

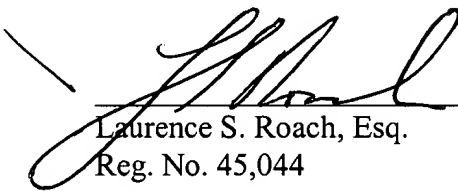
means for emulating a disk access system of a computer using solid state memory
components to store file sequences with compression/decompression algorithm data; and

a memory for storing a program connected to at least the [audio] third digital signal
20 processor, said memory comprising at least two audio conversion programs for converting audio
at first and second respective rates.

The Examiner is invited to telephone the undersigned in regard to this Amendment and the above identified application.

Respectfully submitted,

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Date



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